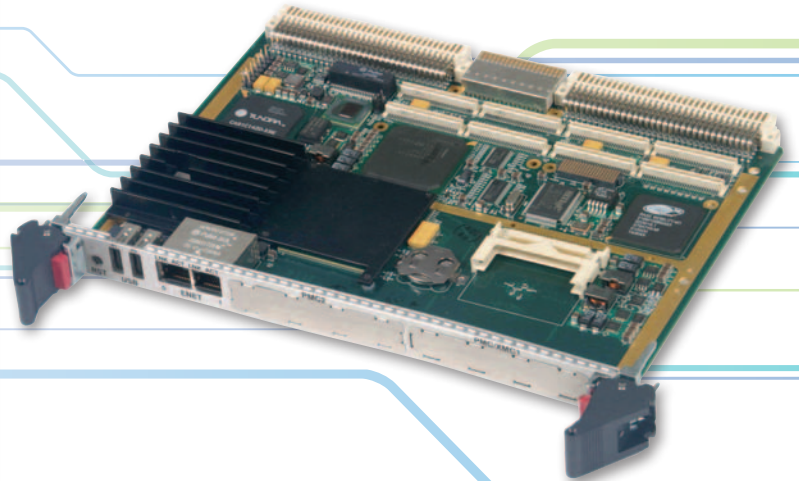


CPU 71-10 (DPD/RPD)

- Legacy VME or VME64 compatible
- Core™2 Duo Mobile Processor
- MIL-STD-810F versions
- Convection and Conduction cooled
- RTM available for rear I/O support



FEATURES

VME Compatible – Tundra Universe IID PCI-VMEbus interface provides VME64 compatibility. Low power 5V only power allows compatibility with older VME backplanes.

High Performance and Low Power – Dual core processing at 30W typical power consumption enables cool operation, even at extended temperature operation.

Gigabit Ethernet – Four Gb Ethernet ports are provided, two on the front and two on the rear, including support for VITA 31.1 backplane switching

MIL-STD Versions – The CPU 71-10 is offered in versions that support wedge locks for high shock and vibration immunity, and conduction-cooling. Conformal coating is available for all versions.

- Defense
- Homeland Security
- Aerospace
- Industrial
- Transportation

The CPU 71-10 is a VMEbus (and VME64) compatible platform based on the Intel® Core™2 Duo Mobile Processor L7400. The DPD takes advantage of the Core-Duo's low 17 W power consumption as a rugged Single Board Computer (SBC). It is optionally available as an IEEE 1101.2-compliant, conduction-cooled VMEbus module with wedge locks and a full-board heat sink for high shock/vibration environments and temperature extremes.

The E7520 Memory Controller Hub (MCH) and 6300ESB I/O Controller Hub (ICH) chipset supports PCI-X and PCIe expansion, USB 2.0, ATA/100, and Serial ATA (SATA). Two Gb Ethernet ports and two USB 2.0 ports are accessible from the front panel in addition to two PMC bezels. On-board CompactFlash permits single-slot booting. Two VITA 31.1-compliant, 10/100/1000BaseTX ports are routed to the backplane. Two SATA ports, VGA video, two Gb Ethernet ports, four RS232 ports, one RS422 port, an IDE interface, PS/2 mouse & keyboard, and two more USB 2.0 ports are routed to the backplane. Two PMC-X sites are provided for additional I/O expansion. Conventional PC I/O is accessible with industry-standard connectors on optional rear I/O modules.

